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Multivibrator - Wikipedia
Multivibrators find applications in a variety of systems where square waves or timed intervals are required. For example, before the advent of low-cost integrated circuits, chains of multivibrators found use as frequency dividers. A free-running multivibrator with a frequency of one-half to one-tenth of the reference frequency would accurately

Edge-triggered Latches: Flip-Flops | Multivibrators
What we need is a digital circuit that outputs a brief pulse whenever the input is activated for an arbitrary period of time, and we can use the output of this circuit to briefly enable the latch. We’re getting a little ahead of ourselves here, but this is actually a kind of monostable multivibrator, which for now we’ll call a pulse detector.

Pass-Transistor-Logic | Digital-CMOS-Design

I-V Characteristics of PMOS Transistor
I-V Characteristics of PMOS Transistor is a graph that shows the relationship between the drain-source current (I DS) and its terminal voltages. In linear region the I DS will increase linearly with increase in drain to source voltage (V DS) whereas in saturation region the I DS is ...

How To Use SocketCAN With C++ In Linux | mbedded.ninja
Jul 21, 2017 · The padding in the above struct allows data to be aligned to a 64-bit boundary. This allows the user to define their own structs and unions to easily access the data (by casting). For example, you could access all 8-bytes of data as a single 64-bit value if desired.

BASIC ELECTRONICS LAB
7) Find and studies the different configuration of multivibrators using NE 555. 8) Design and construct switched mode power supply, voltage regulator using regulator IC chip 9) Construction of simple function generator using IC chip. 10) Study of a digital to analog converter and vice versa. Facilities available: 1.

How To Use SocketCAN With The Command-Line In Linux
Jul 21, 2017 · To send data to the CAN bus, use the cansend utility:

```
~$ cansend can0 123#1122334455667788
```

The above command will send a CAN message on can0 with the identifier 0x123 and the data bytes \{0x11, 0x22, 0x33, 0x44, 0x55, 0x66, 0x77, 0x88\}.

JK Flip Flop and the Master-Slave JK Flip Flop Tutorial
The Master-Slave JK Flip Flop. The Master-Slave Flip-Flop is basically two gated SR flip-flops connected together in a series configuration with the slave having an inverted clock pulse. The outputs from Q and \(\bar{Q}\) from the "Slave" flip-flop are fed back to the inputs of the "Master" with the outputs of the "Master" flip flop being connected to the two inputs of the "Slave" flip flop.

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CD4046B data sheet, product information and support | TI.com
CD4046B CMOS Micropower Phase-Locked Loop (PLL) consists of a low-power, linear voltage-controlled oscillator (VCO) and two different phase comparators having a common signal-input amplifier and a common comparator input.